THE UNITED STATES PATENT AND TRADEMARK OFFICE

MACATHAMECC DATORYS &	
MCGUINNESS, PATRICK J. § Sue Date: 4/4/2006	
In re Application of: MCGUINNESS, PATRICK J. S Issue Date: 4/4/2006 S Issue Date: 4/4/2006 S Examiner: Vuthe Siek S Group Art Unit: 2825 S Docket No.: SC12009TS	
Filed: 09-08-2003 § Group Art Unit: 2825	
§ Docket No.: SC12009TS Title: METHOD FOR AUTOMATED TRANSISTOR FOLDING	
Certificate of Submission I hereby certify that this correspondence is being USPTO, Alexandria, VA.	g submitted to the
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Commissioner for Patents Alexandria, VA 22313

SUBMISSION OF CERTIFICATE OF CORRECTION

Dear Commissioner:

Enclosed is a Certificate of Correction listing error(s) in the subject patent.

Please enter these corrections. Since the error(s) appear to be on the part of the Applicant, the required fee will be charged to Deposit Account 503079 via the Electronic Filing System.

SÍNGH, RANJEEV

Attorney for Applicant(s) Registration No. 47093

Respectfully submitted,

Telephone No. (512) 996-6839 Facsimile No. (512) 996-6854 Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION Page __1__ of ___1__ 7124385 B2 PATENT NO.: 10/657609 APPLICATION NO: DATE: 09-08-2003 FIRST NAMED INVENTOR: MCGUINNESS, PATRICK J. It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below: In Column 20, Line 18, Claim No. 3: Change "create modified fold solution from" to--creating modified fold solution from--In Column 22, Line 59, Claim No. 17: Change "create modified fold solution from" to--creating modified fold solution from--

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Freescale Semiconductor, Inc. Law Department 7700 West Parmer Lane PL02 Austin, TX 78729